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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	09/902,622
Filing Date	July 12, 2001
First Named Inventor	Jun KOYAMA, et al
Group Art Unit	2672
Examiner Name	Anthony J. Blackman
Attorney Docket Number	0756-2339

Sheet 1 of 3

U.S. PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
nk		4,330,363		Biegesen et al	05/18/1982	
nk		4,466,179		Kasten	08/21/1984	

FOREIGN PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
		Office ³	Number ⁴	Kind Code ² (if known)				
nk		JP	62-019855		02/05/1987		Full	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
nk		M. Bonnel et al., IEEE Electron Dev. Lett., 14(12) (1993) 551 "...TFTs with two step annealing process"	
		I. Fujieda et al., IEDM, '93 Proc., page 587, "Self-referenced Polys-Si TFT amplifier..."	
		M. Fuse et al., Sol. St. Phenom, 37-38 (1994) 565, "Performance of poly-si TFTs..."	

Examiner Signature	<i>Anthony J. Blackman</i>	Date Considered	2/22/06
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